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APPLICANTS

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** CONTINUING DATA *****

yes GCR 5/16/05

This application is a CON of 10/406,860 04/04/2003 PAT 6,799,240
 which is a CON of 10/247,114 09/18/2002
 which is a CON of 09/512,133 02/23/2000 PAT 6,496,887
 which is a CON of 09/039,923 03/16/1998 PAT 6,038,627

** FOREIGN APPLICATIONS *****

IF REQUIRED, FOREIGN FILING LICENSE GRANTED

** 06/01/2004

Foreign Priority claimed <input type="checkbox"/> yes <input checked="" type="checkbox"/> no	STATE OR	SHEETS	TOTAL	INDEPENDENT
35 USC 119 (a-d) conditions met <input type="checkbox"/> yes <input checked="" type="checkbox"/> no <input type="checkbox"/> Met after Allowance	COUNTRY	DRAWING	CLAIMS	CLAIMS
Verified and Acknowledged Examiner's Signature _____	CA	6	1	1

ADDRESS

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TITLE

SRAM bus architecture and interconnect to an FPGA

FILING FEE RECEIVED 770	FEES: Authority has been given in Paper No. _____ to charge/credit DEPOSIT ACCOUNT No. _____ for following:	<input type="checkbox"/> All Fees <input type="checkbox"/> 1.16 Fees (Filing) <input type="checkbox"/> 1.17 Fees (Processing Ext. of time) <input type="checkbox"/> 1.18 Fees (Issue) <input type="checkbox"/> Other _____ <input type="checkbox"/> Credit
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